

Notice of References Cited	Application/Control No. 09/943,456	Applicant(s)/Patent Under Reexamination SWOBODA, GARY L.	
	Examiner Russ Guill	Art Unit 2123	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Schieber, Colleen D. et al., "Ratchet: Real-time Address Trace Compression Hardware for Extended Traces," Performance Evaluation Review, vol. 21, #3 and 4, Apr. 1994, pp. 22-32
	V	Eric E. Johnson et al.; "Lossless Trace Compression", February 2001, IEEE Transactions on Computers, Volume 50, Number 2
	W	A. R. Pleszkun. "Techniques for Compressing Program Address Traces", 27th Annual IEEE/ACM International Symposium on Microarchitecture, pp. 32-40. November 1994
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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